

FIG. 1

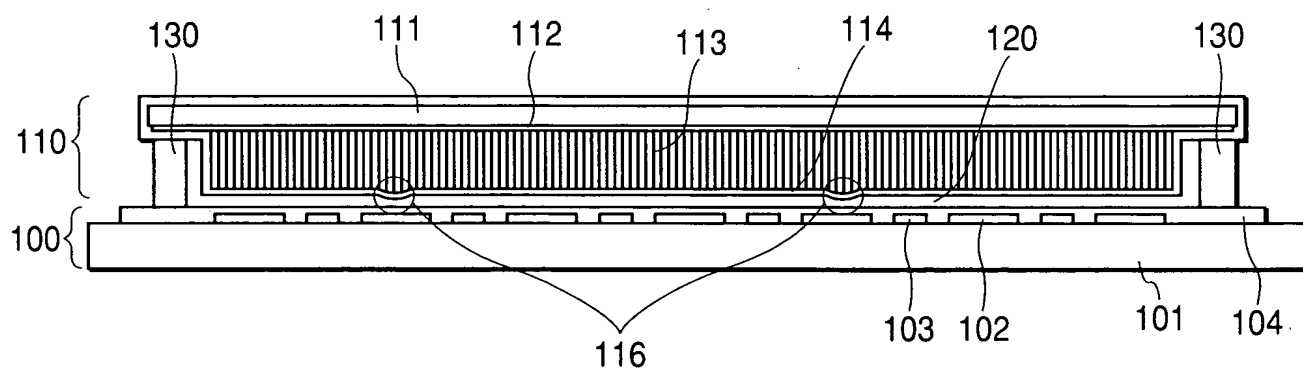


FIG. 2

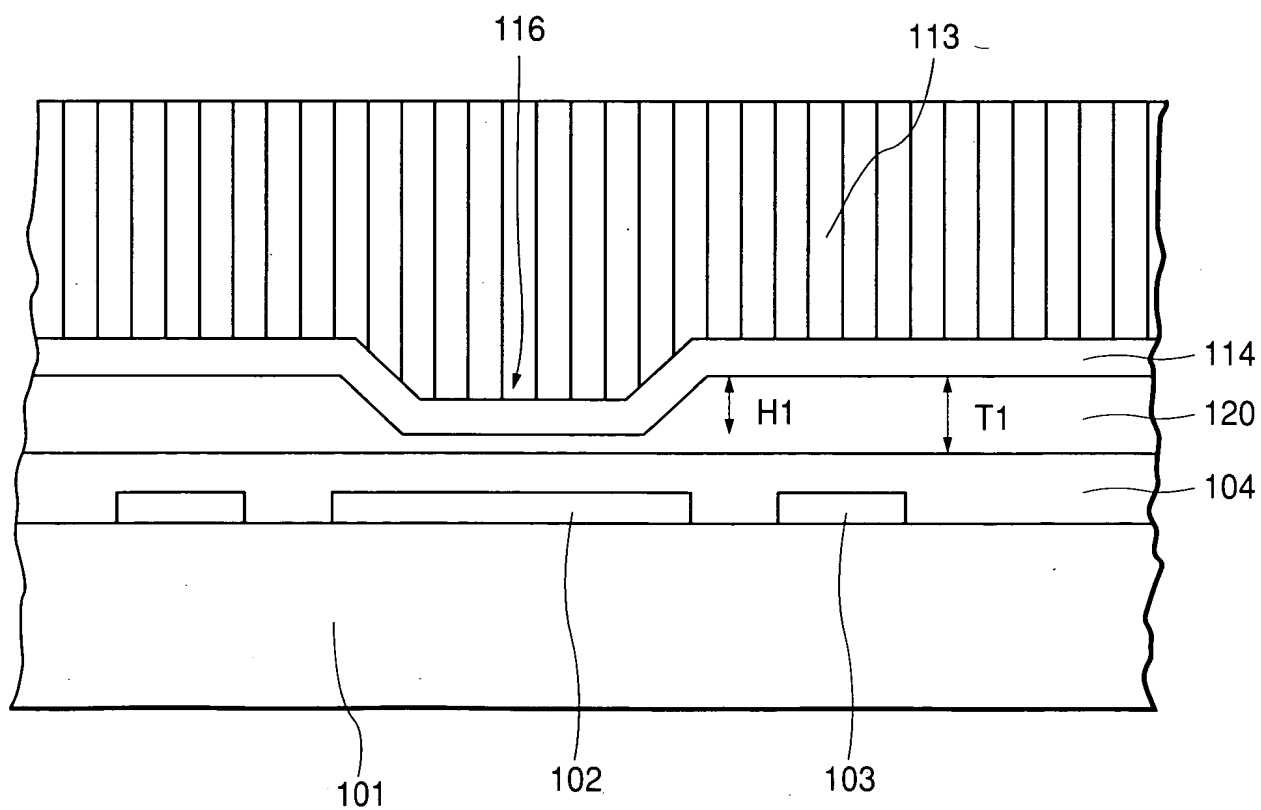


FIG. 3A

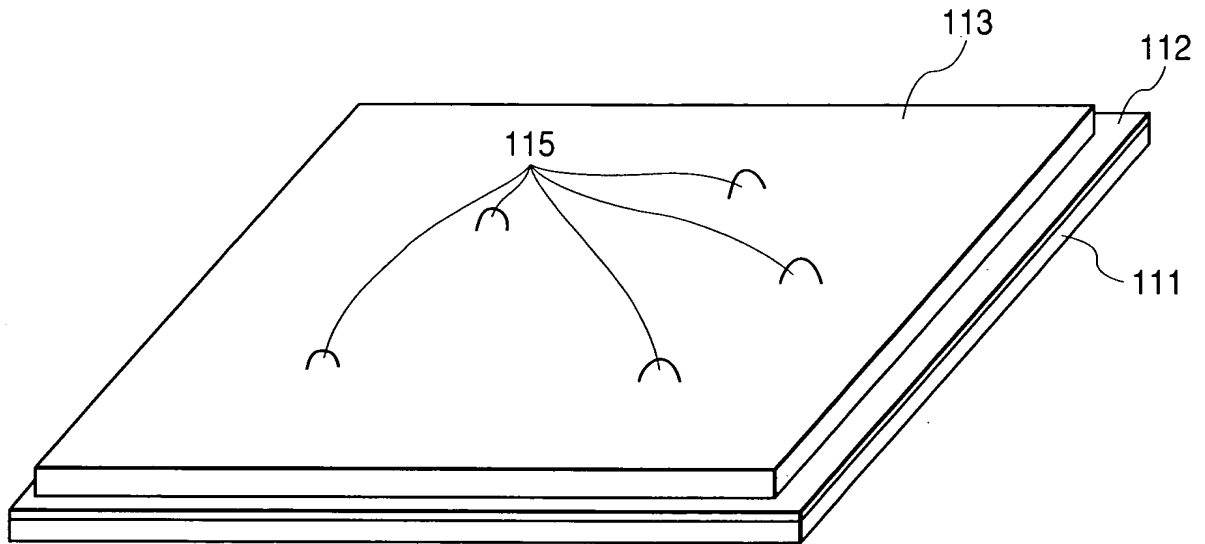


FIG. 3B

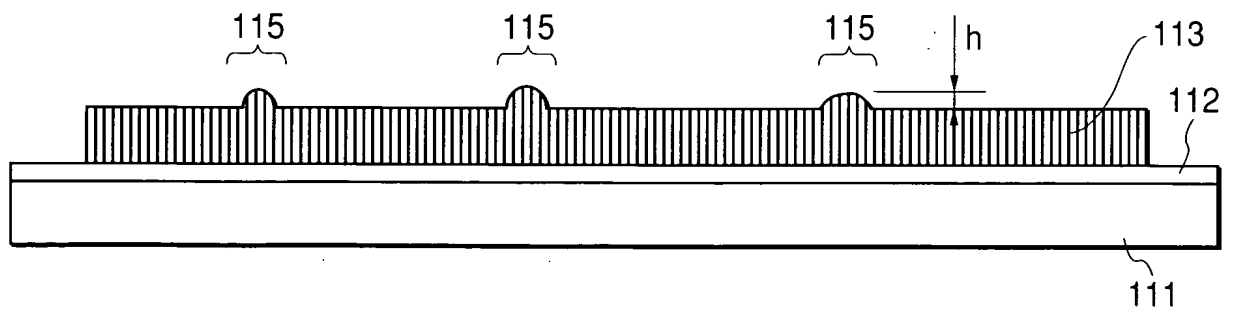


FIG. 4A

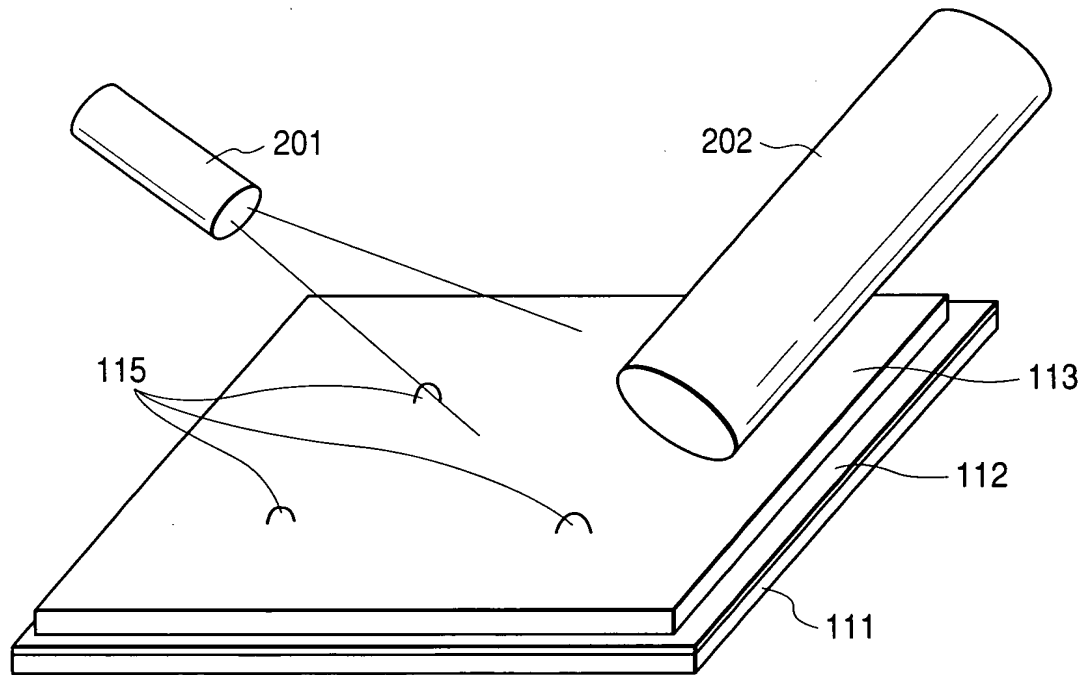


FIG. 4B

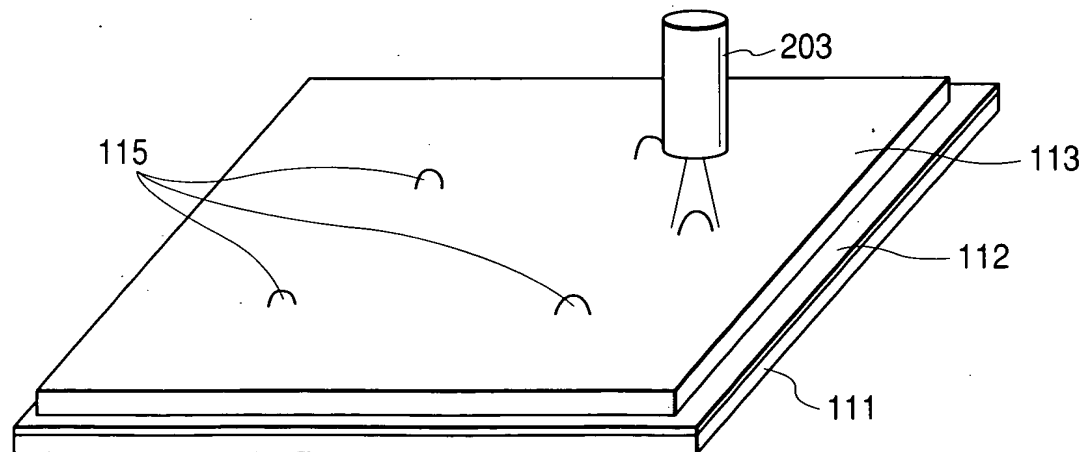


FIG. 5A

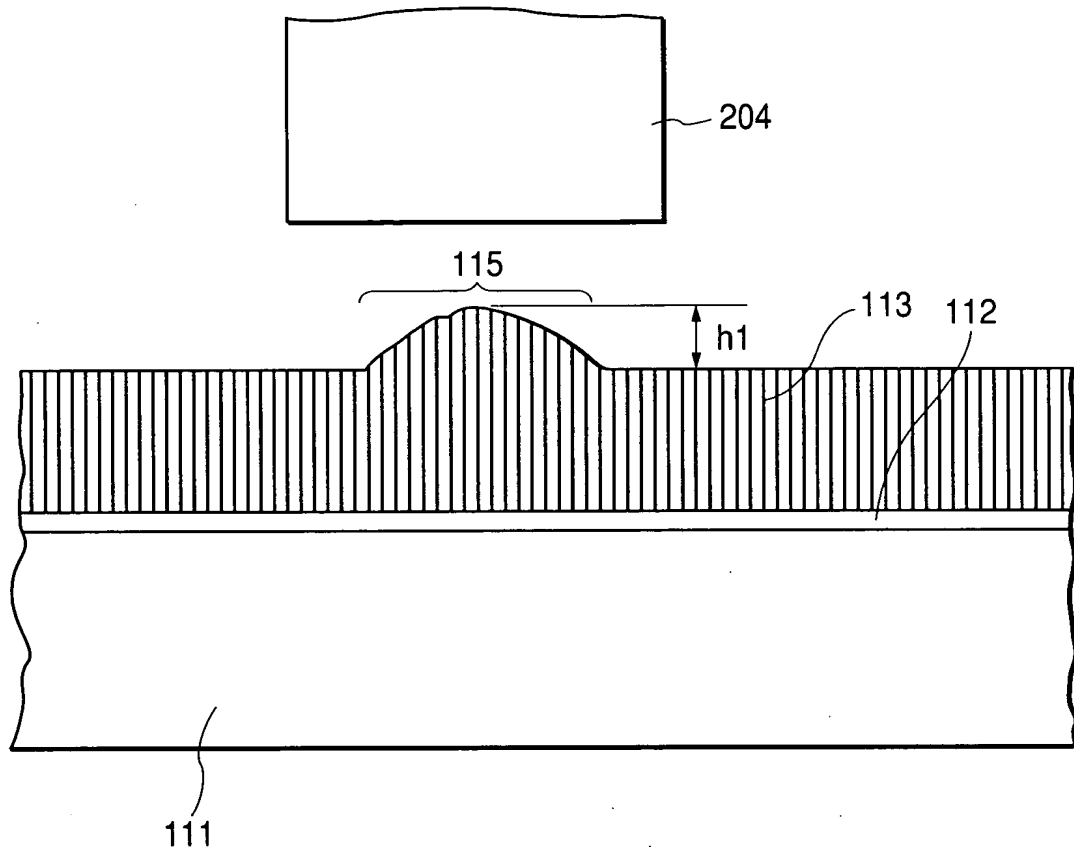


FIG. 5B

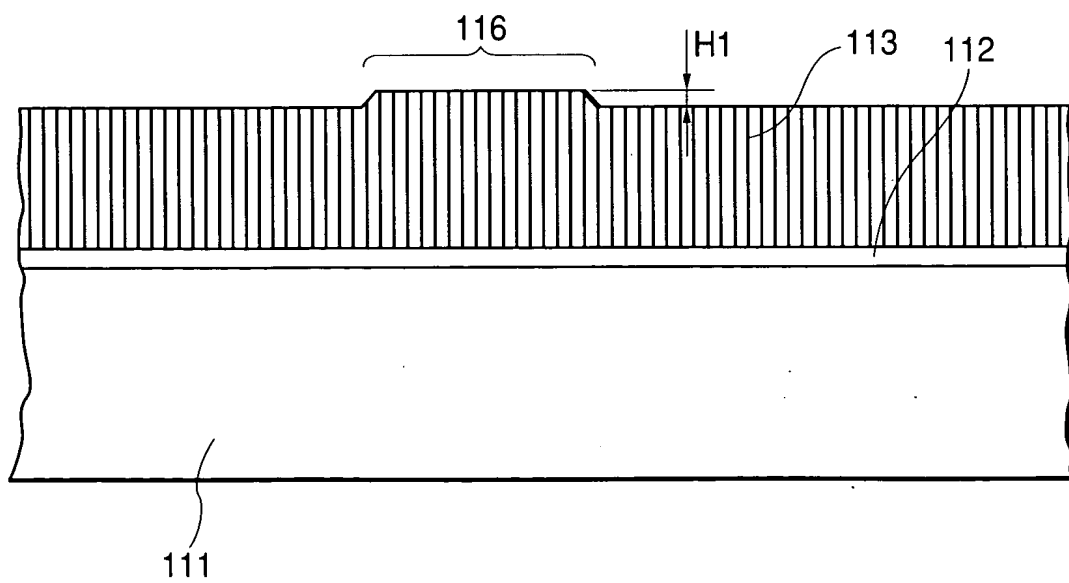


FIG. 6A

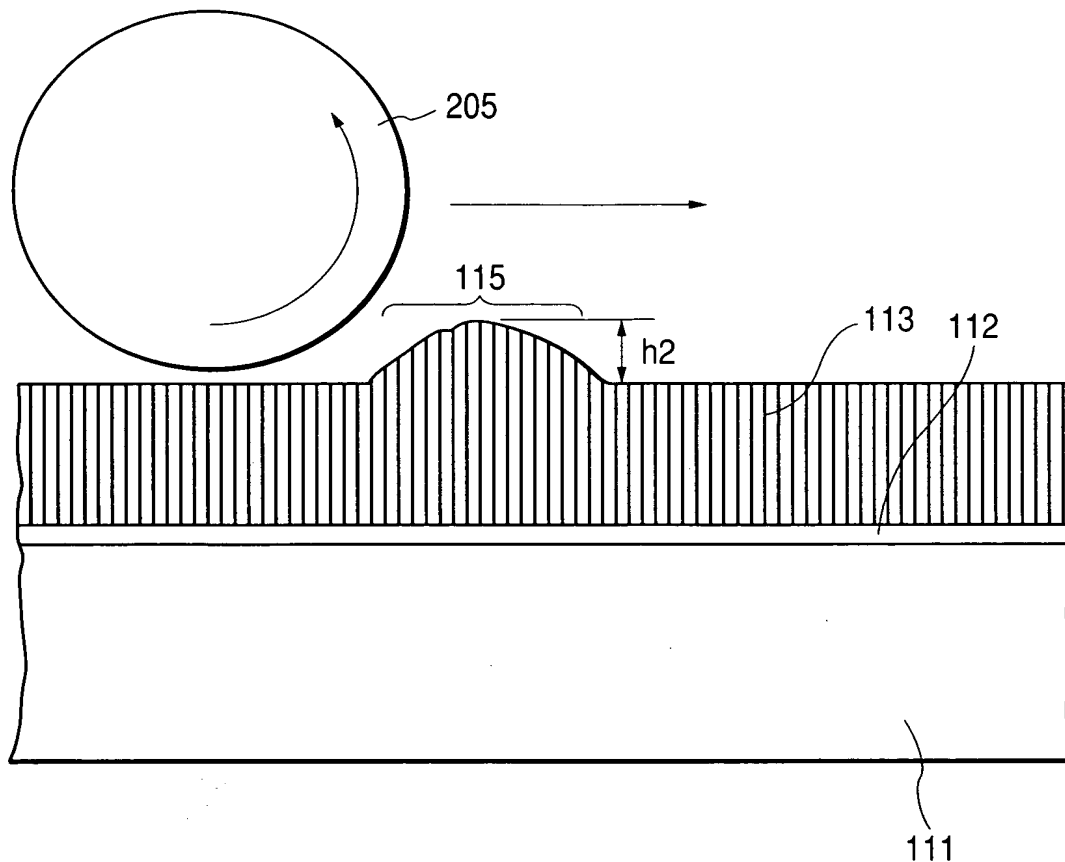


FIG. 6B

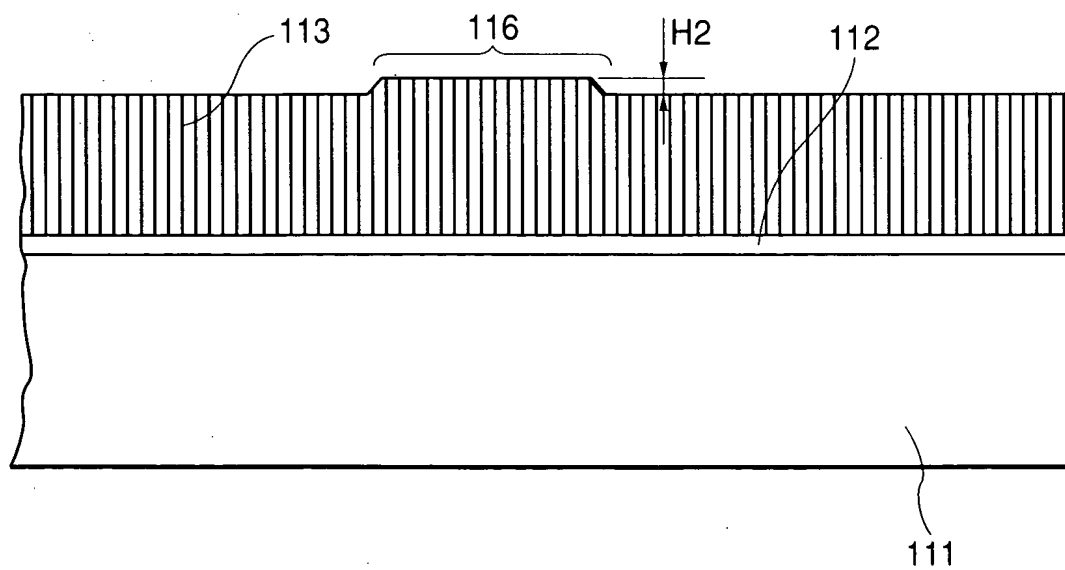


FIG. 7A

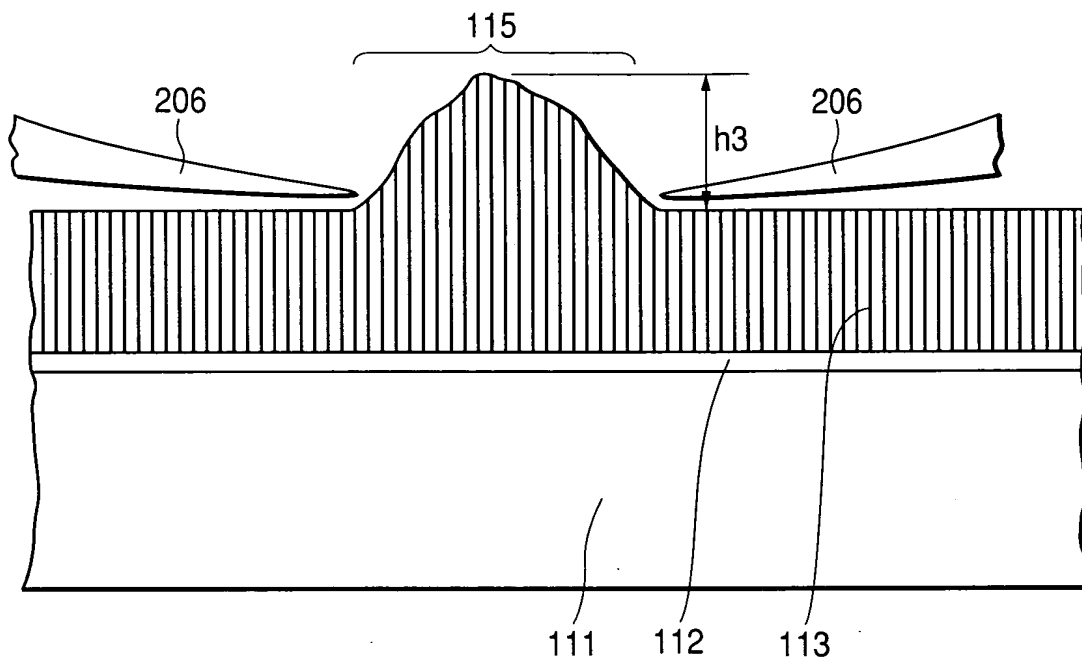


FIG. 7B

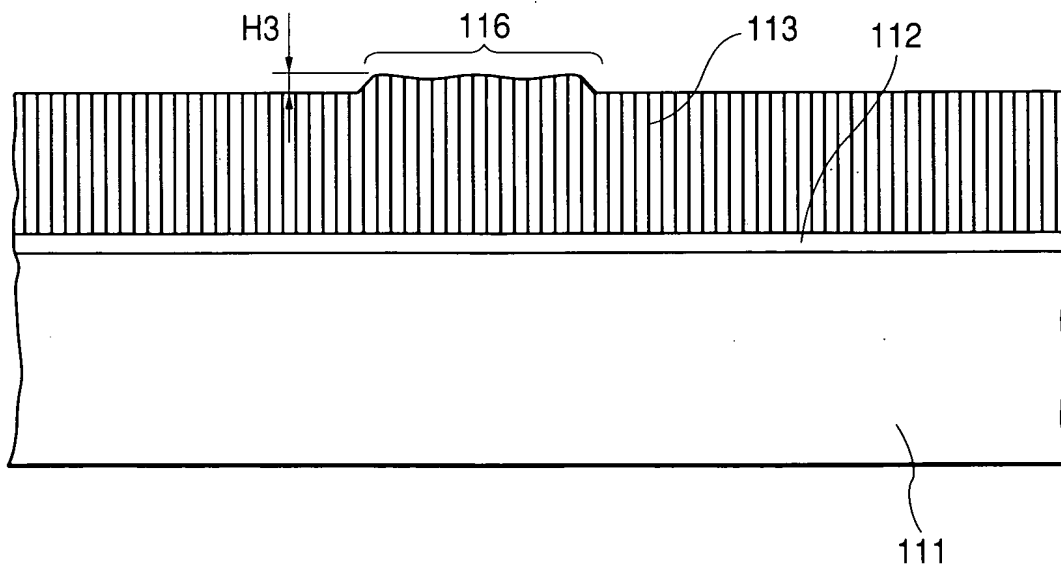


FIG. 8A

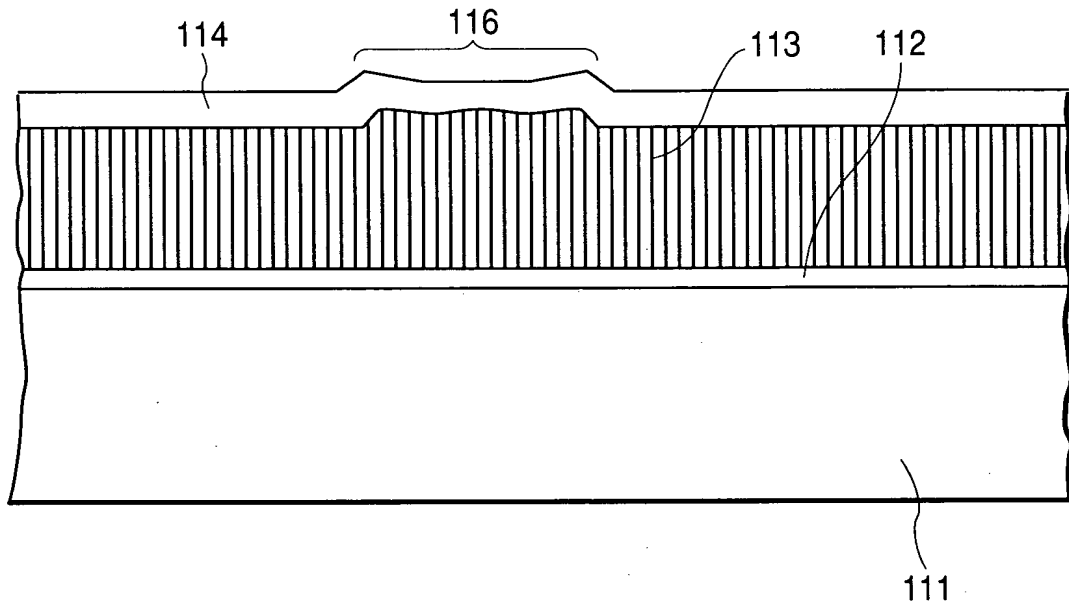


FIG. 8B

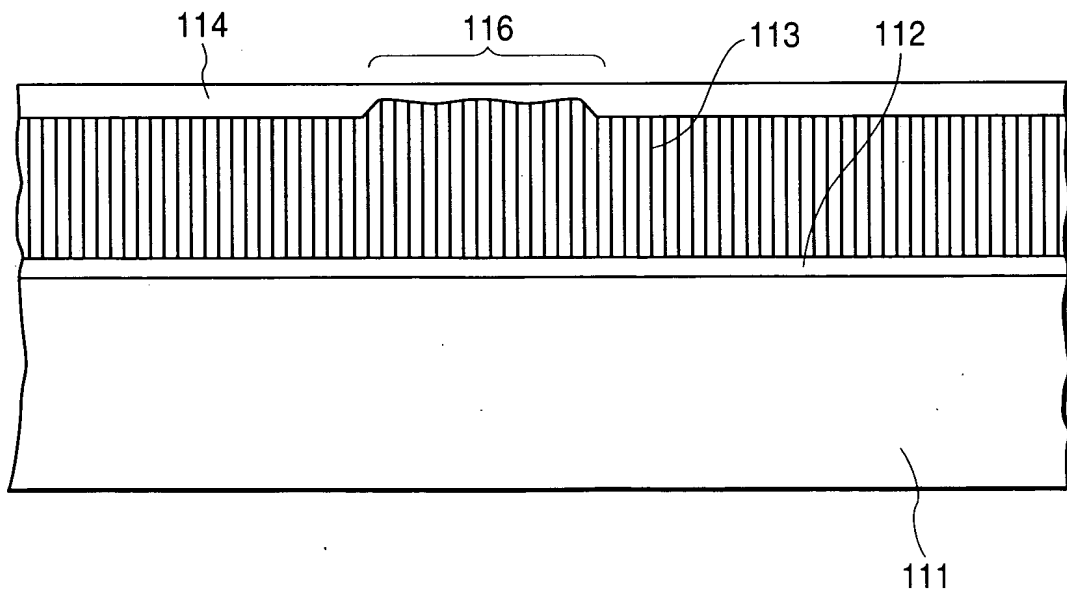


FIG. 9A

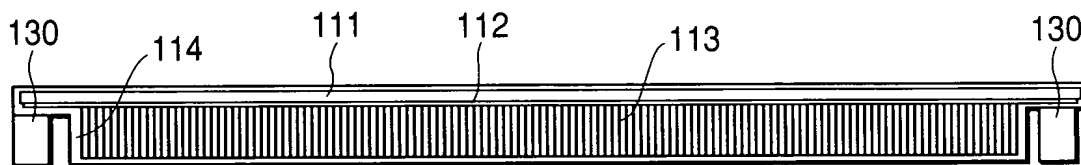
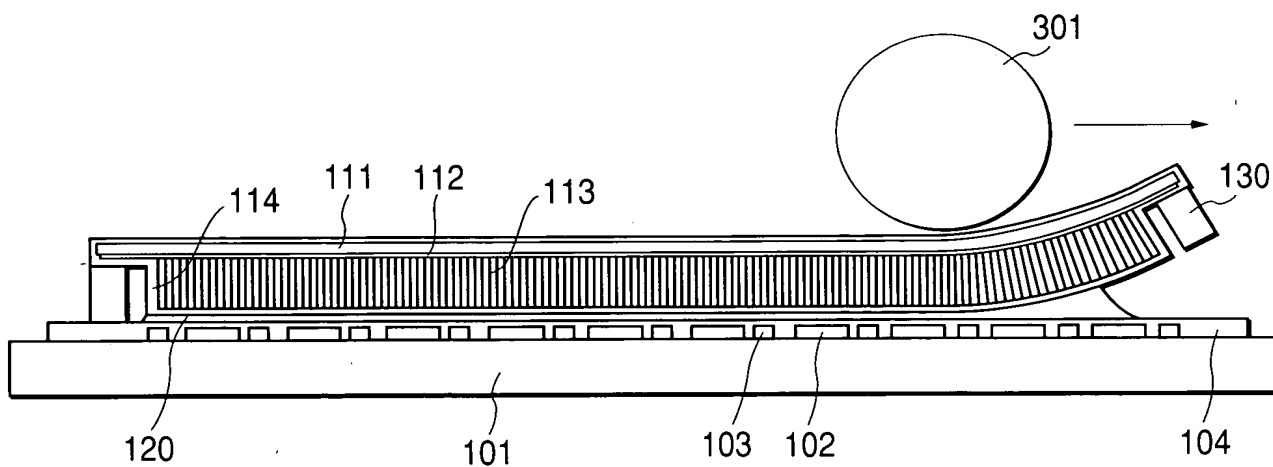


FIG. 9B



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FIG. 10

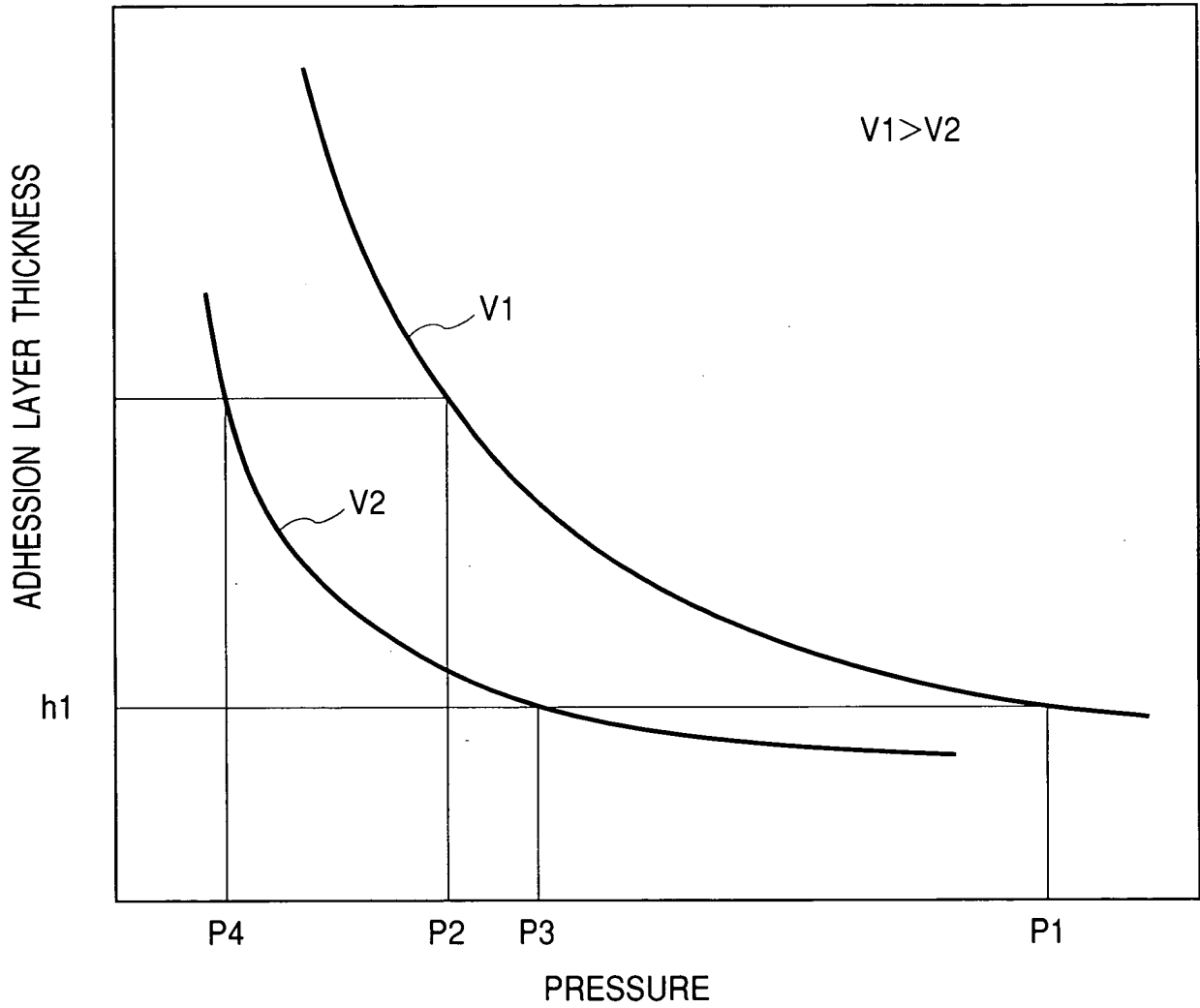


FIG. 11

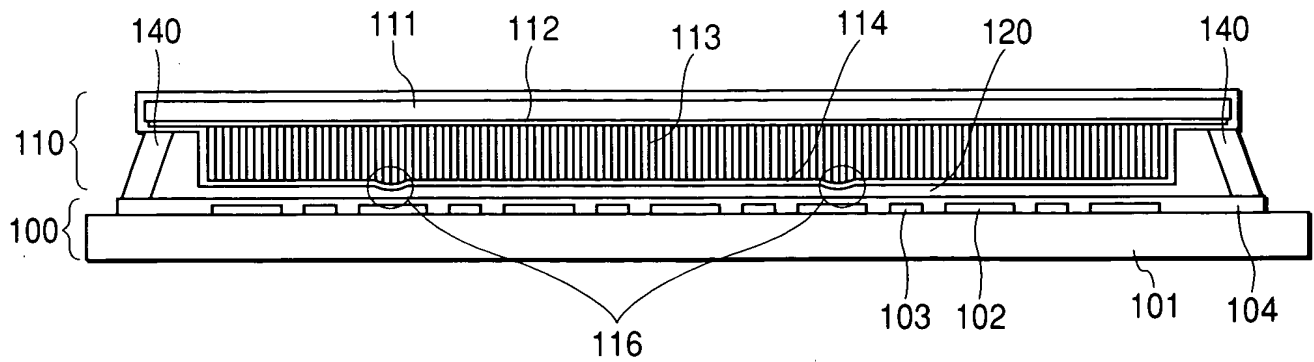


FIG. 12

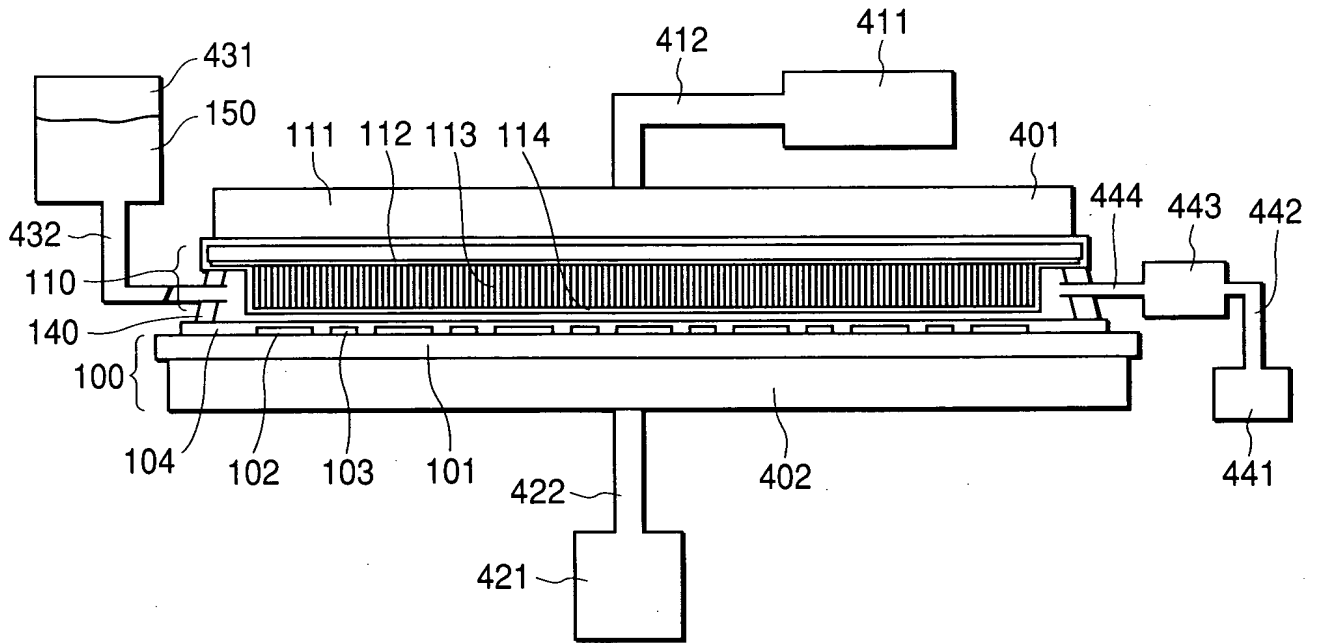


FIG. 13

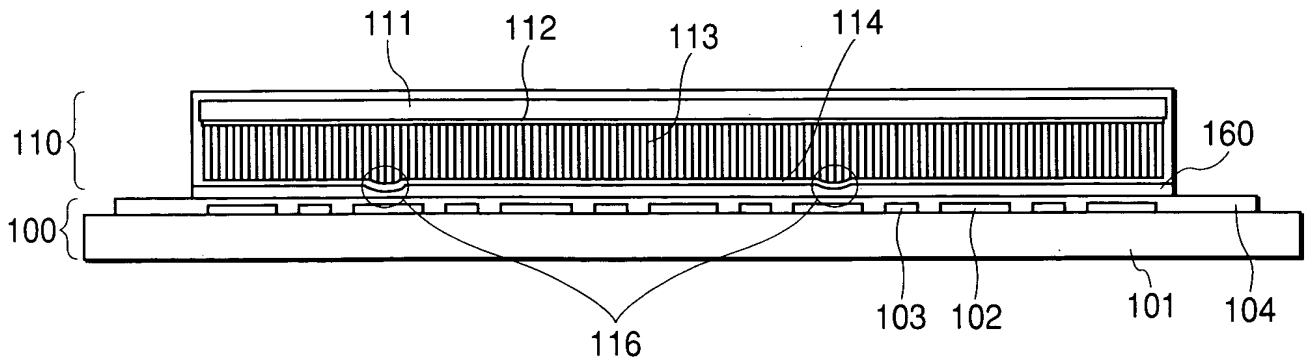


FIG. 14A

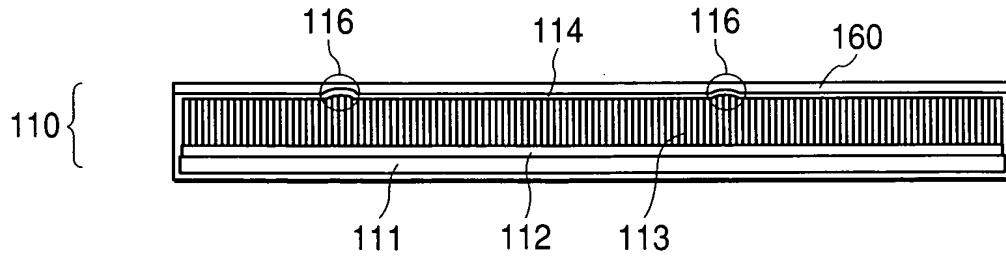


FIG. 14B

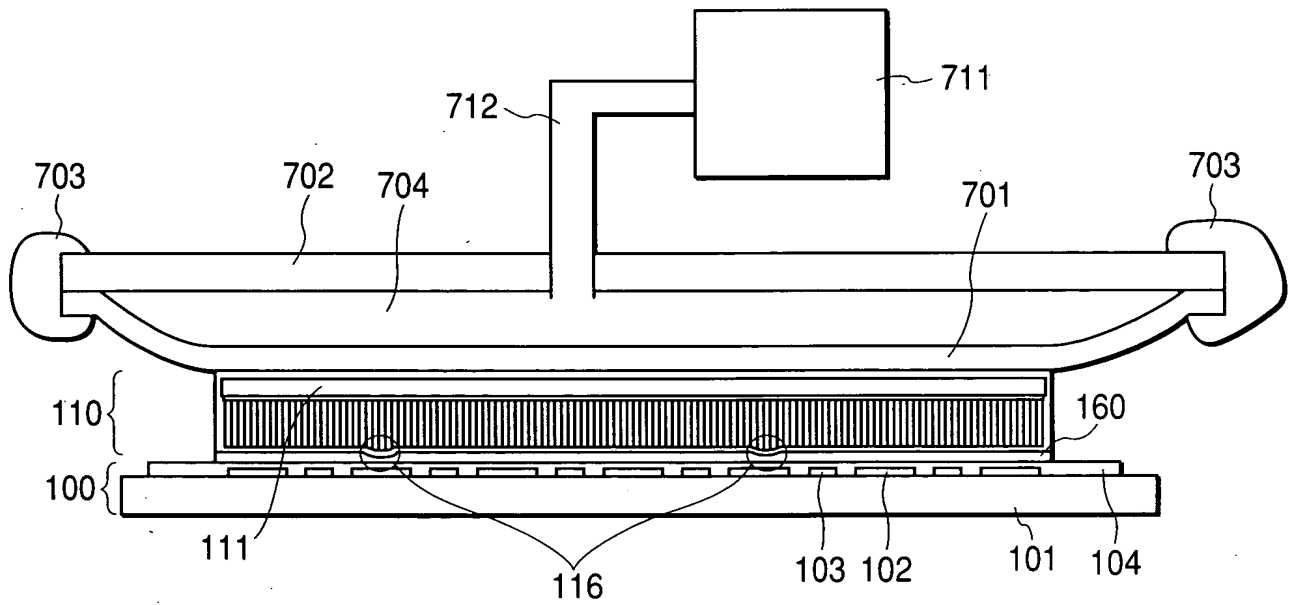
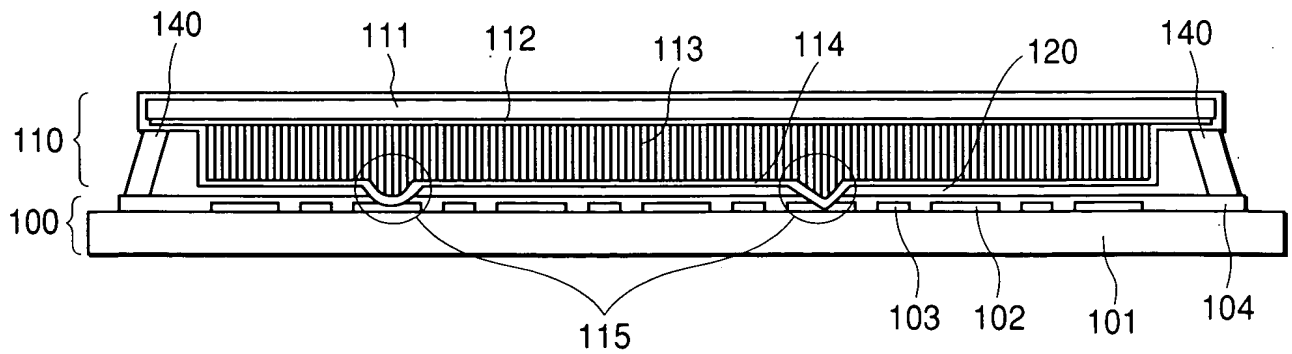


FIG. 15



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FIG. 16A

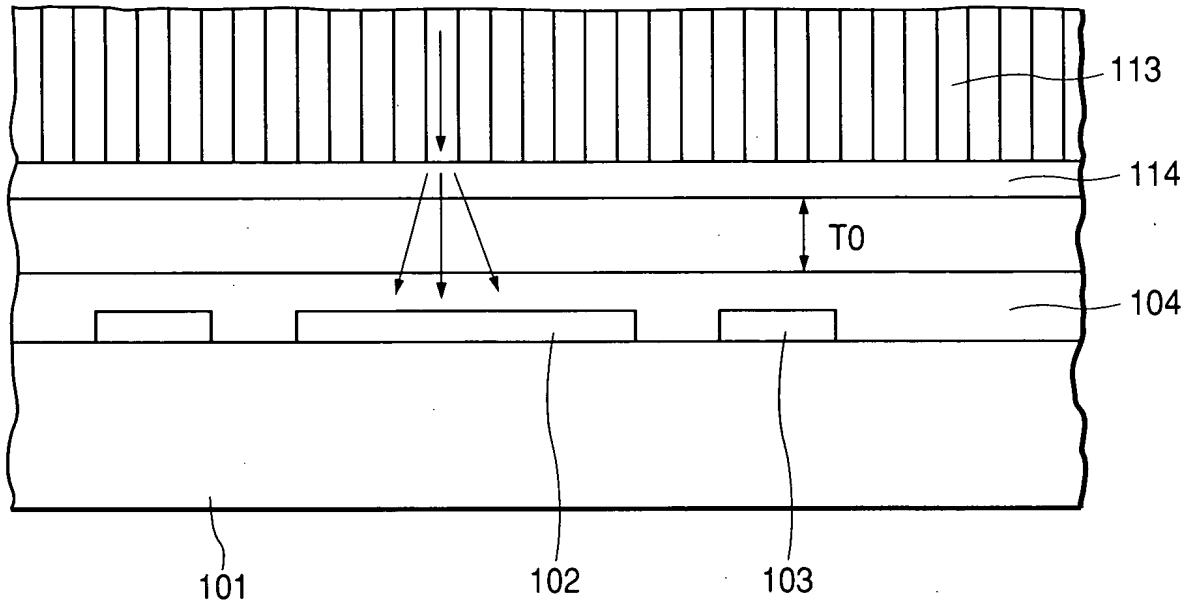
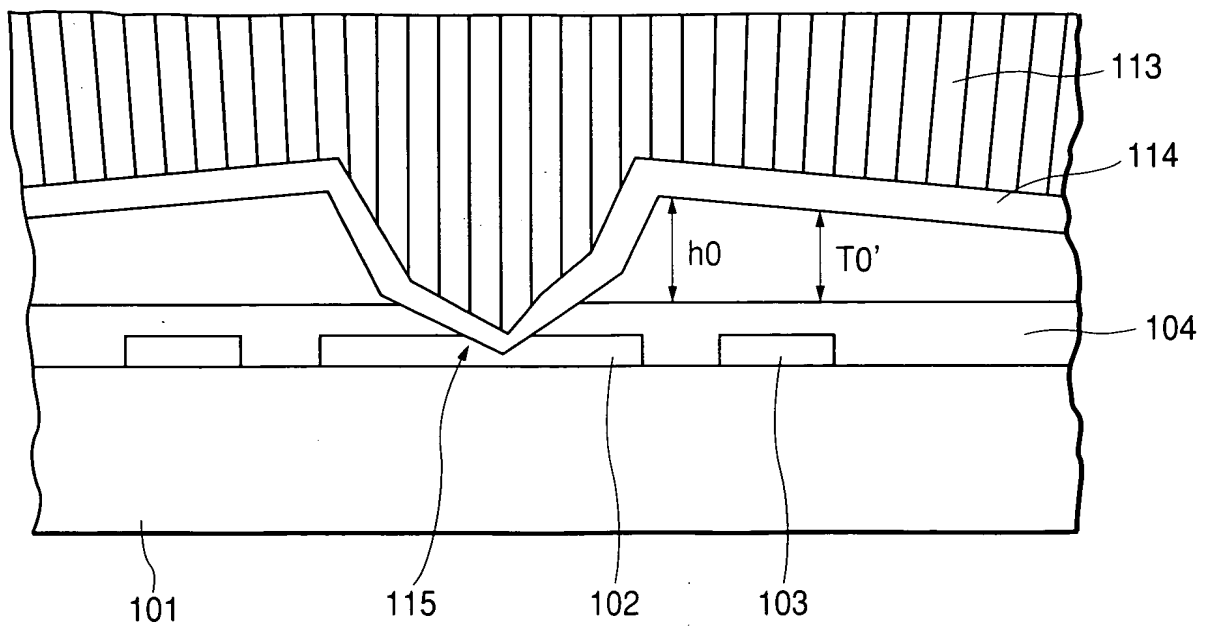


FIG. 16B



The diagram illustrates a 3-bit digital-to-analog converter (DAC) circuit. It features a 3x3 array of NMOS transistors (503) and capacitors (501, 502). The top row of capacitors is connected to a common input line (510). The bottom row of capacitors is connected to ground. The output of the DAC is taken from the node (505) where the top row of capacitors meets the first column of transistors. This output is connected to a feedback loop consisting of a switch (504) and a current source (508). The feedback loop is controlled by a digital input (507). The output of the DAC is also connected to a load (509). The circuit is designed to convert a 3-bit digital input into an analog output.

The circuit diagram shows a 3-bit digital-to-analog converter (DAC) implemented using a 3x3 array of current sources and a 3-bit digital-to-analog converter (DAC) block. The array consists of three rows and three columns of current sources, each formed by a PMOS transistor and a capacitor. The PMOS transistors are connected to a common PMOS supply line (610) and their sources are connected to a common NMOS supply line (607). The capacitors are connected to the gates of the PMOS transistors and their other terminals are connected to the gates of the NMOS transistors. The NMOS transistors are connected to the common NMOS supply line (607) and their gates are connected to the gates of the PMOS transistors. The outputs of the array are connected to a 3-bit digital-to-analog converter (DAC) block (609) via three inverters (604). The DAC block (609) is connected to a common output line (608) which is connected to ground. The DAC block (609) is also connected to a common output line (608) which is connected to ground. The DAC block (609) is also connected to a common output line (608) which is connected to ground.

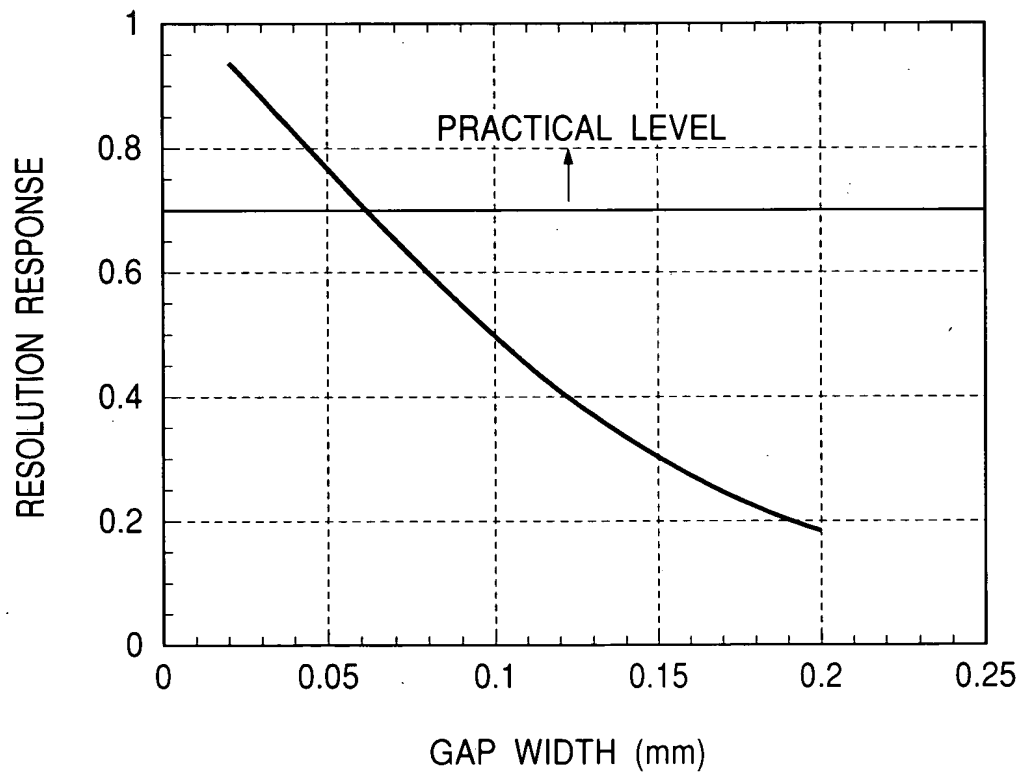
FIG. 19

FIG. 20A

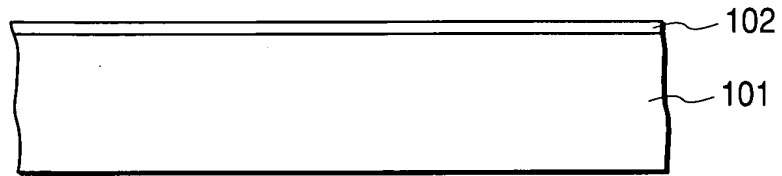


FIG. 20B

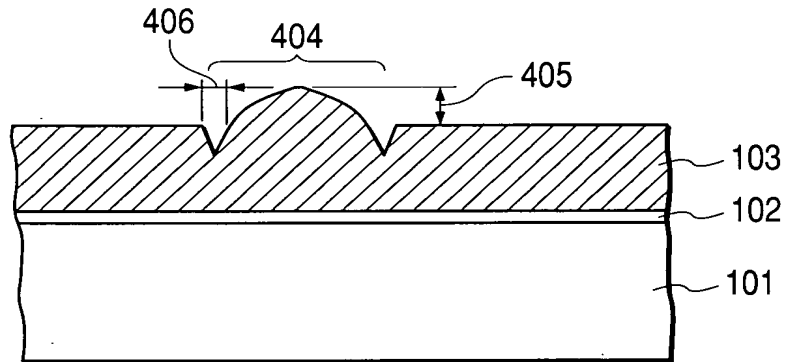


FIG. 20C

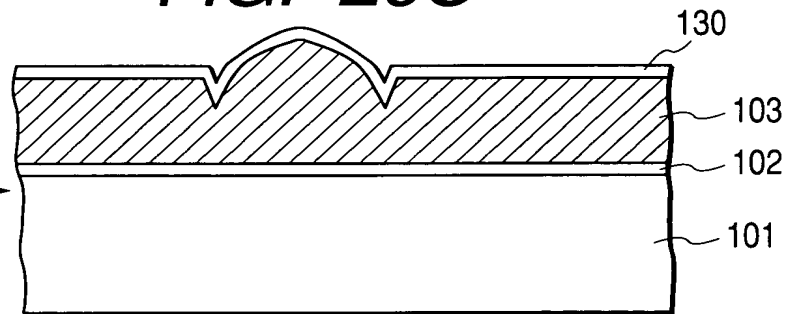


FIG. 20D

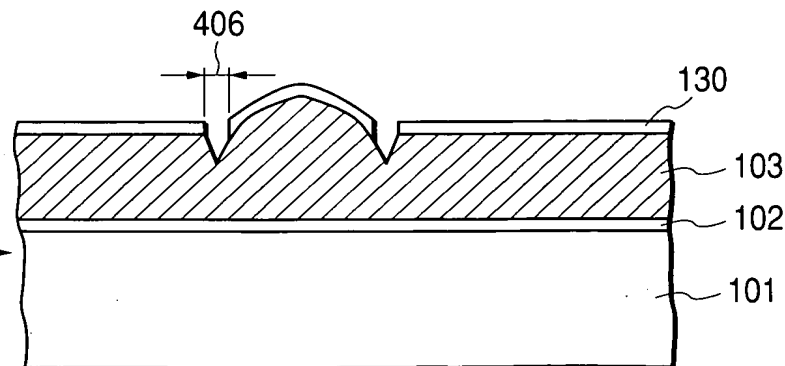


FIG. 21A

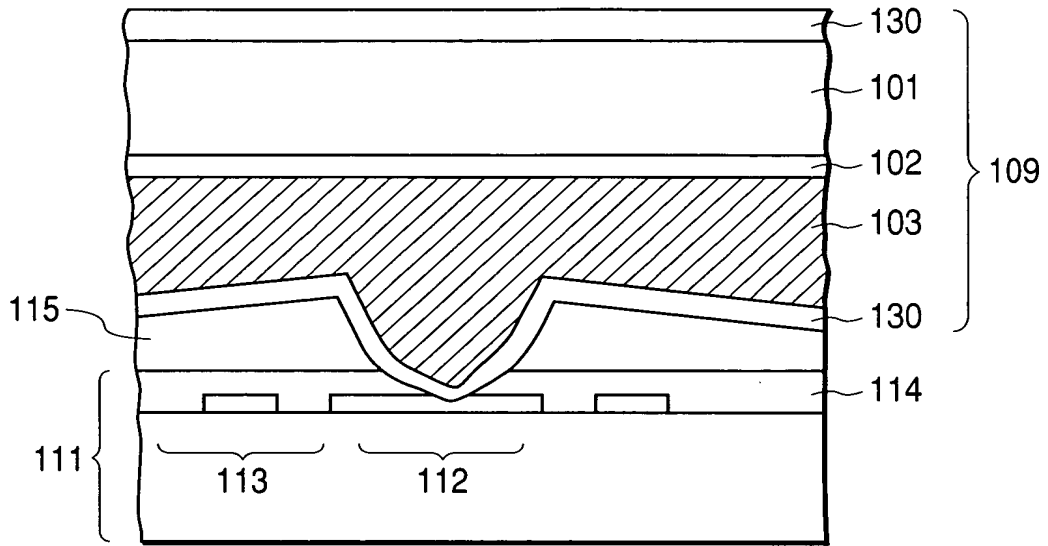


FIG. 21B

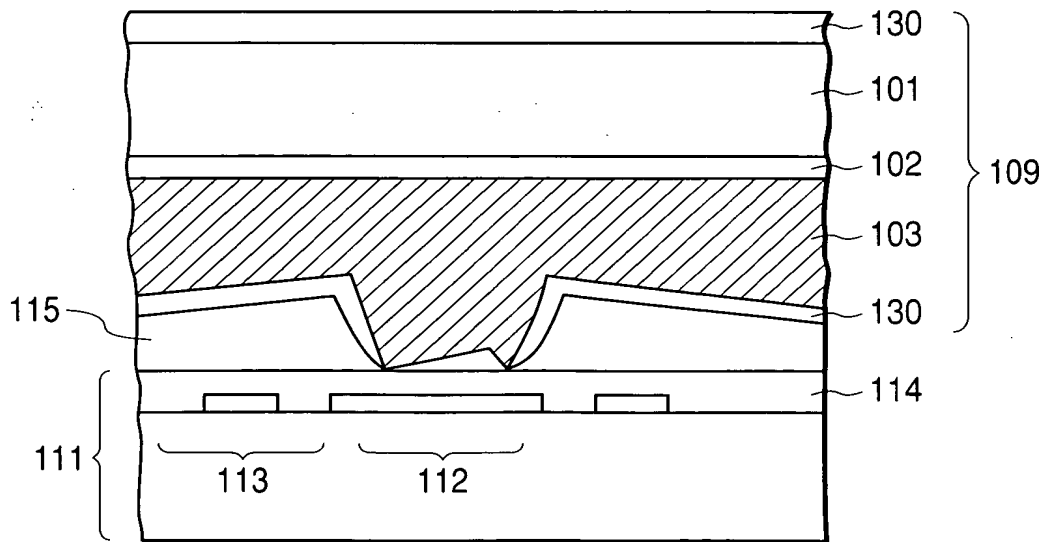


FIG. 22A

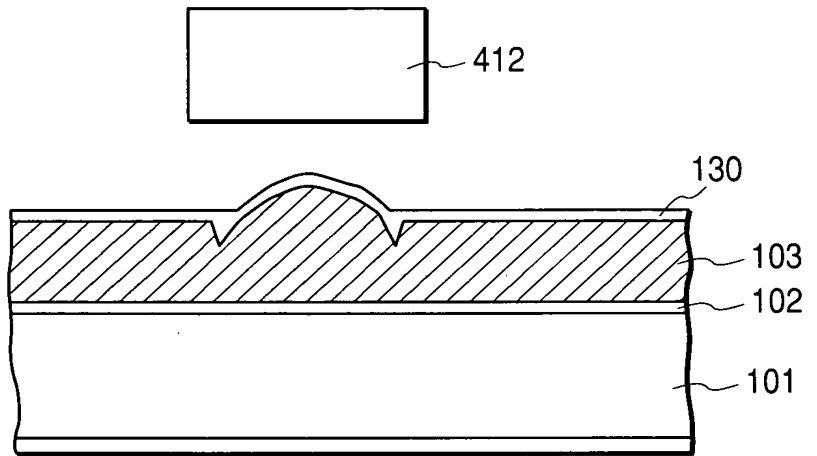


FIG. 22B

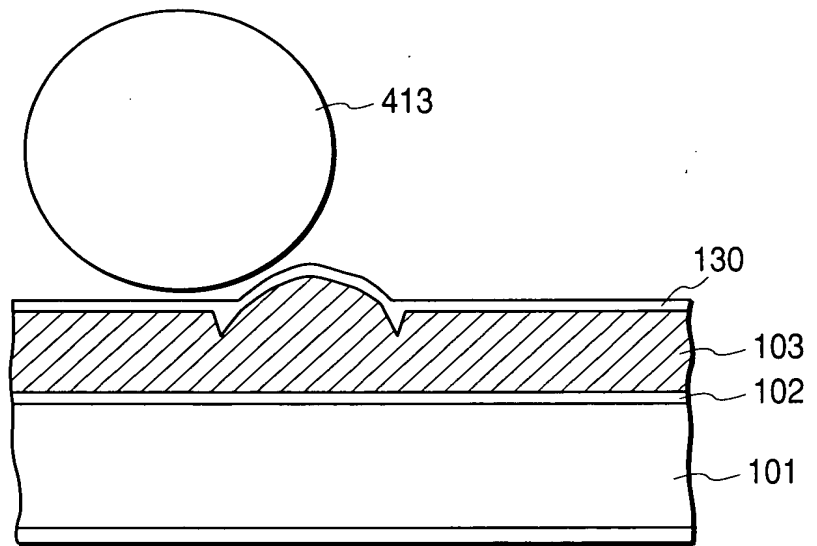


FIG. 22C

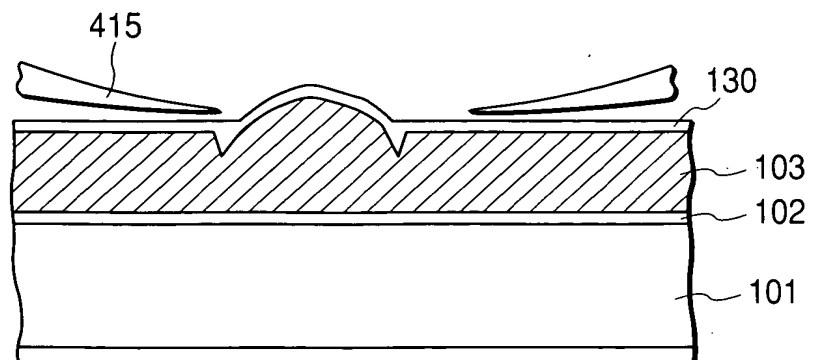


FIG. 23A

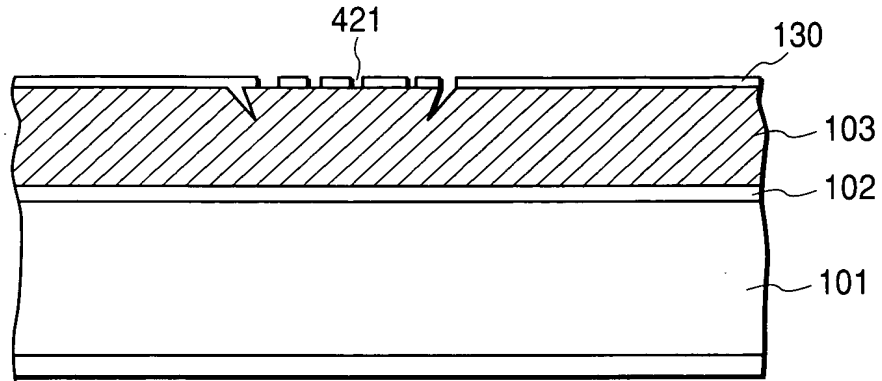


FIG. 23B

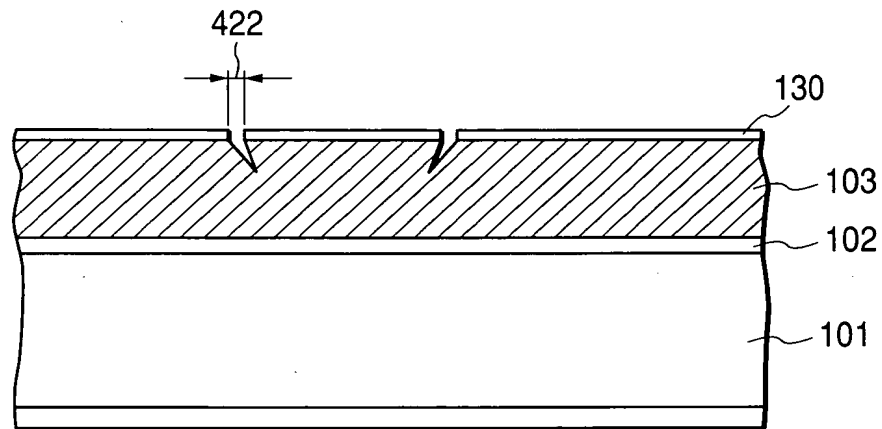


FIG. 23C

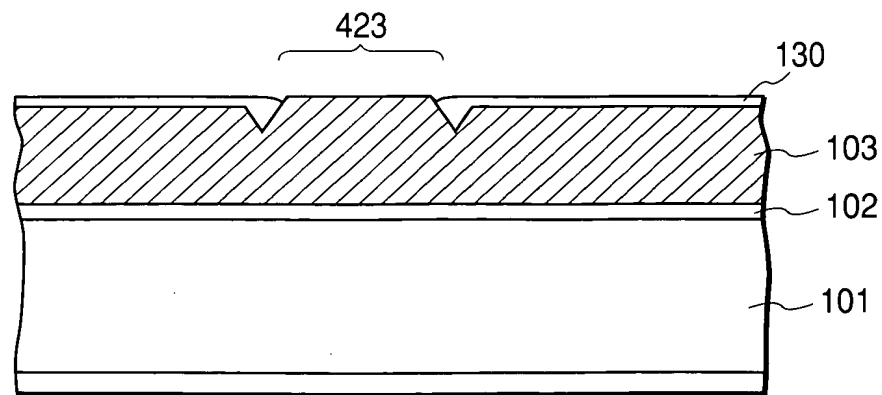


FIG. 24B

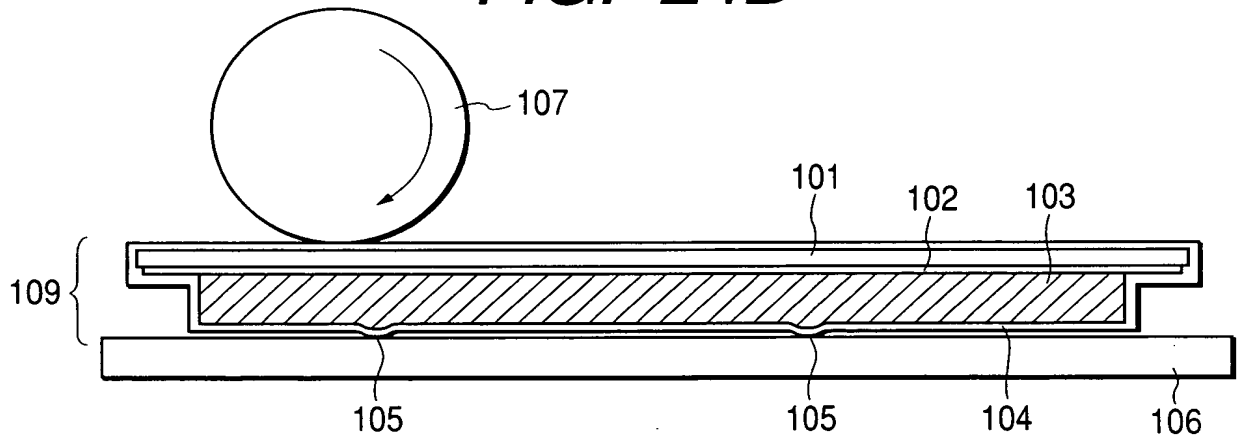


FIG. 24D

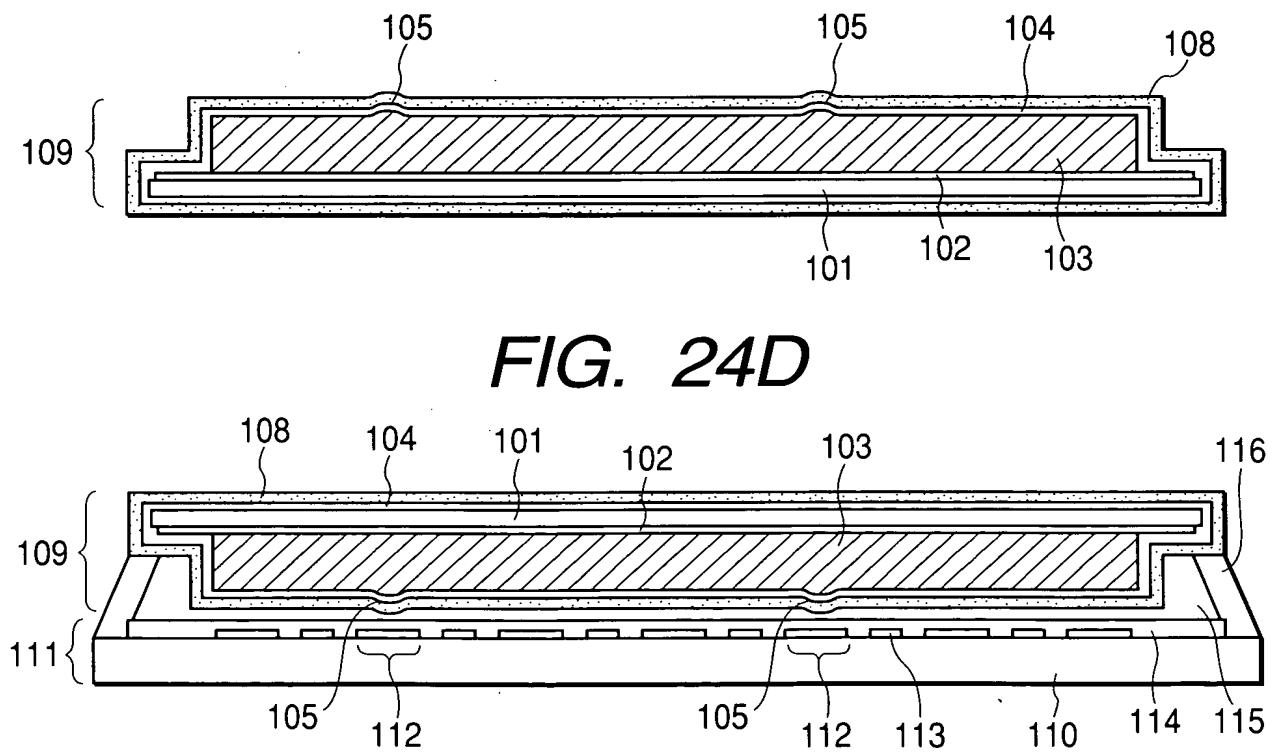


FIG. 25A

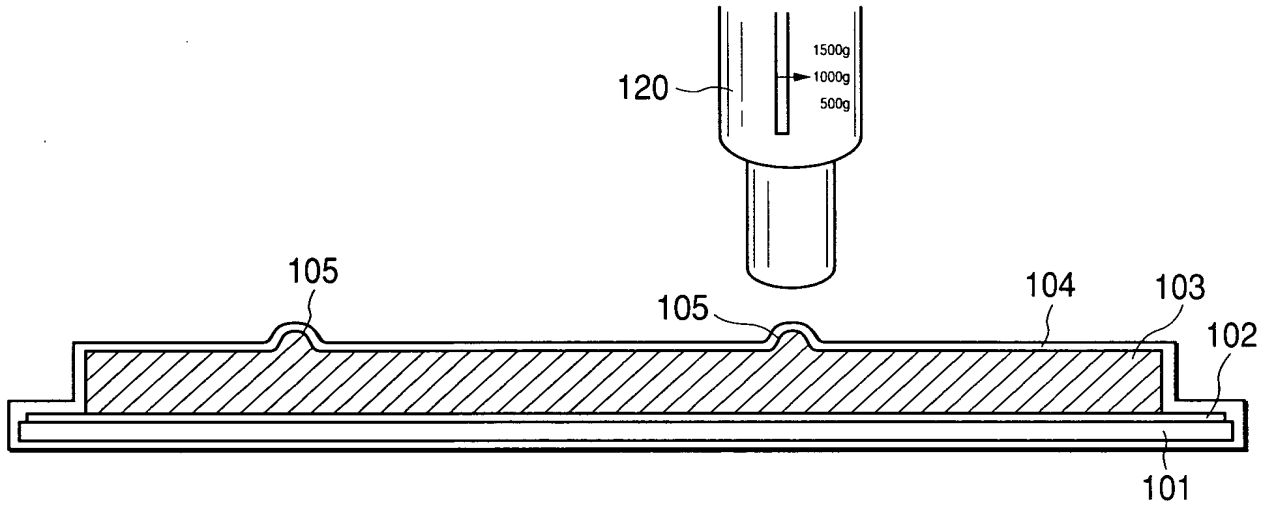


FIG. 25B

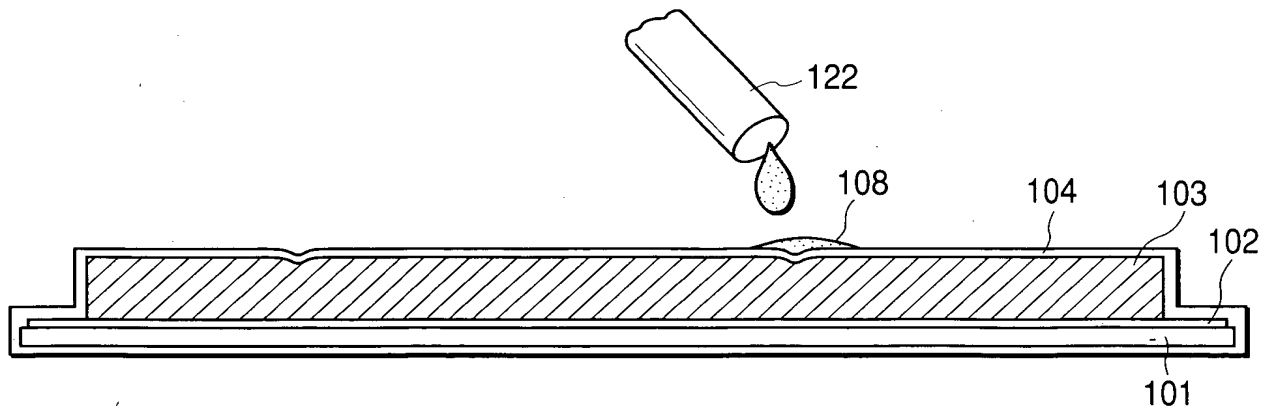


FIG. 26

